

INFORMATION CITED BY APPLICANT THAT MAY BE MATERIAL TO THE
PROSECUTION OF THE SUBJECT APPLICATION

Applicant: P.H. Mawet

Attorney Docket No. MEIP117200

Application No.: 09/898,674

Group Art Unit: 2857

Filed: July 3, 2001

Examiner: --

Title:

LOW VOLTAGE LOW POWER SIGNAL PROCESSING SYSTEM AND METHOD
FOR HIGH ACCURACY PROCESSING OF DIFFERENTIAL SIGNAL INPUTS
FROM A LOW POWER MEASURING INSTRUMENT

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document No.	Kind Code	Date (mm/dd/yyyy)	Name
AS	U1	3,831,113		8/20/1974	Ahmed
	U2	3,886,485		5/27/1975	Takahashi
	U3	3,921,101		11/18/1975	McCoy et al.
	U4	3,995,232		11/30/1976	Laugesen
	U5	4,115,748		9/19/1978	Kubo et al.
	U6	4,205,279		5/27/1980	Beutler
	U7	4,283,690		8/11/1981	Tarbouriech
	U8	4,370,628		1/25/1983	Henderson et al.
	U9	4,377,790		3/22/1983	Zobel et al.
	U10	5,345,195		9/06/1994	Cordoba et al.
	U11	5,886,519		3/23/1999	Masreliez et al.
	U12	5,901,458		5/11/1999	Andermo et al.
	U13	5,982,318		11/09/1999	Yiannoulos
	U14	6,144,330		11/07/2000	Hoffman et al.
AS	U15	6,177,901	B1	1/23/2001	Pan et al.

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Cite No.	Document No.	Kind Code	Publication Date (mm/dd/yyyy)	Country	English Abstract Provided	Translation Provided
None							

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OTHER INFORMATION

(Including Author, Title, Date, Pertinent Pages, Etc.)

*Examiner Cite
Initial No.

CSJ O1 Johns, D. and K. Martin, *Analog Integrated Circuit Design*, John Wiley and Sons, Inc., 1997, "Advanced Current Mirrors and Opamps" and "Comparators."

Examiner

Albert M. S.

Date Considered

4/17/03

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<u>CSJ</u> F1 DE 199 45 949 A1 03/29/2001 Germany X					

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